OIPE IAP80 DEC 05 2006 PATENT & TRADEMARK OFFICE

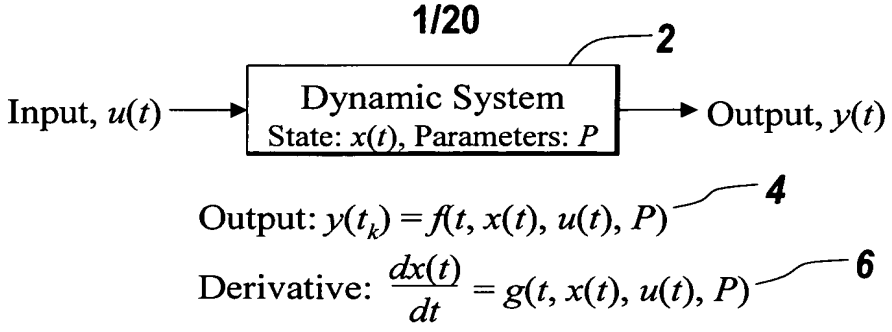

Fig. 1A
(prior art)

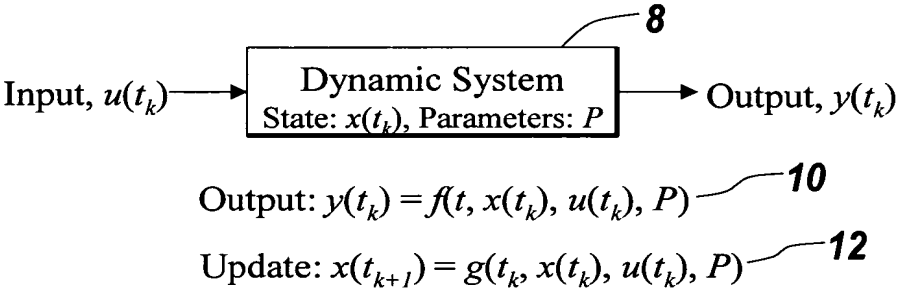

Fig. 1B
(prior art)

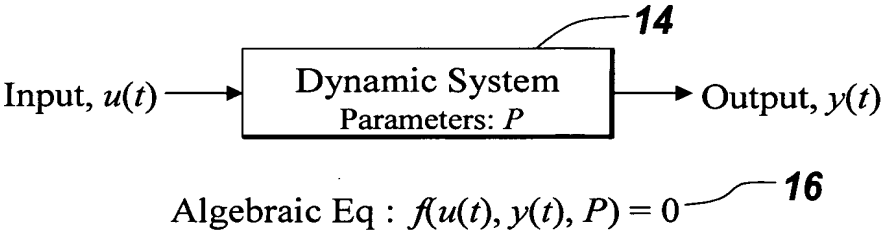

Fig. 1C
(prior art)

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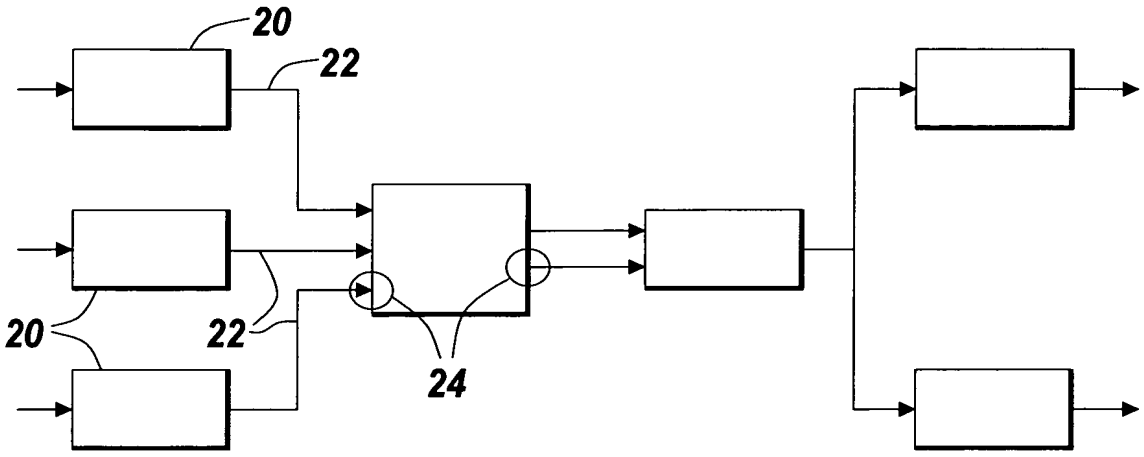
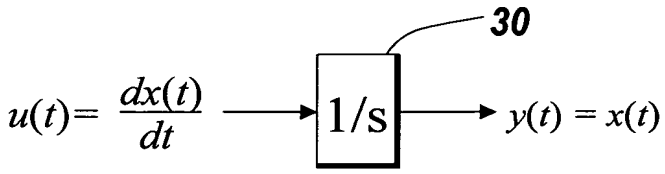


Fig. 2
(prior art)



State, $x(t)$
$x(t_0) = P_{IC}$

Fig. 3
(prior art)

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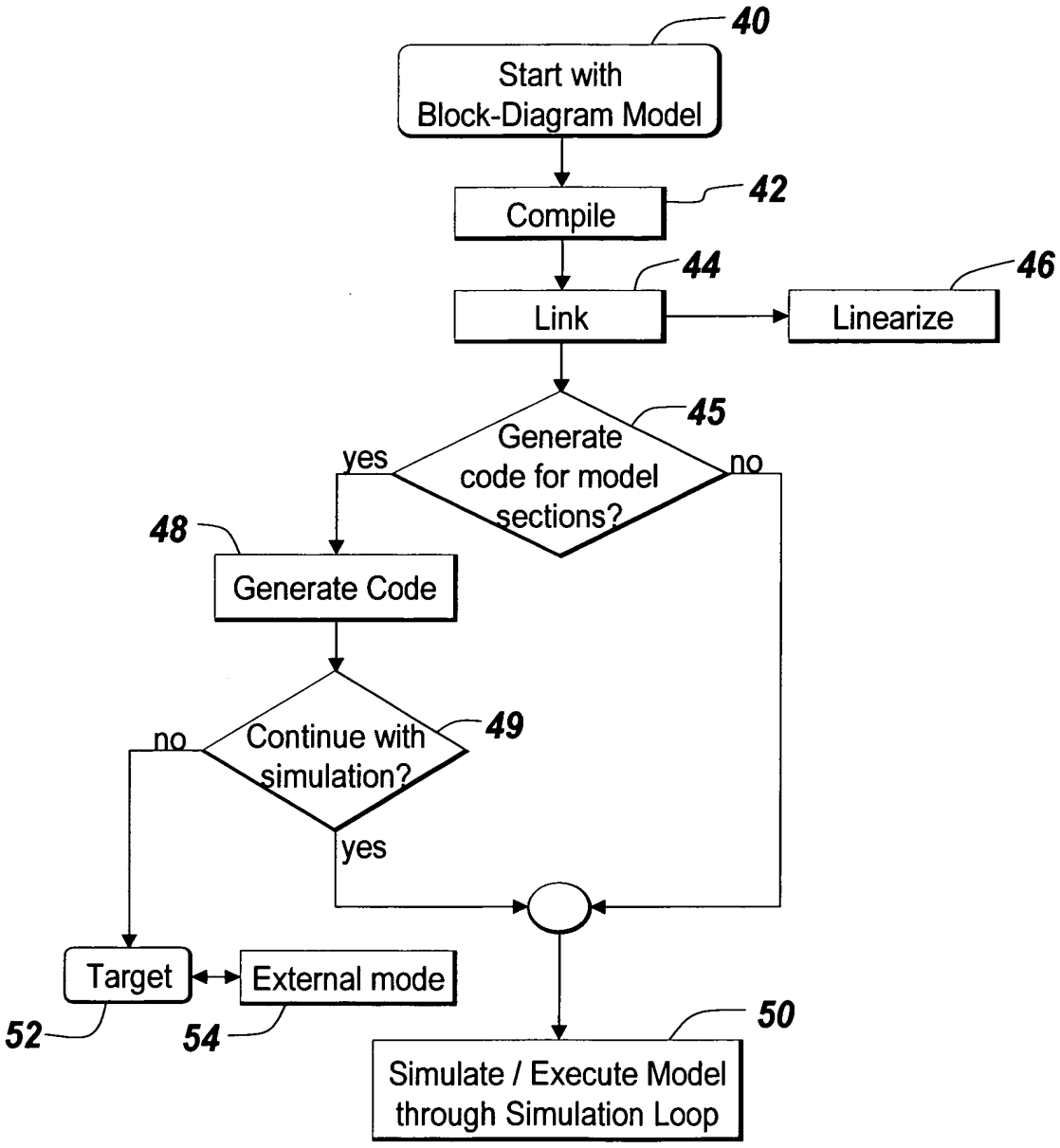

Fig. 4
(prior art)

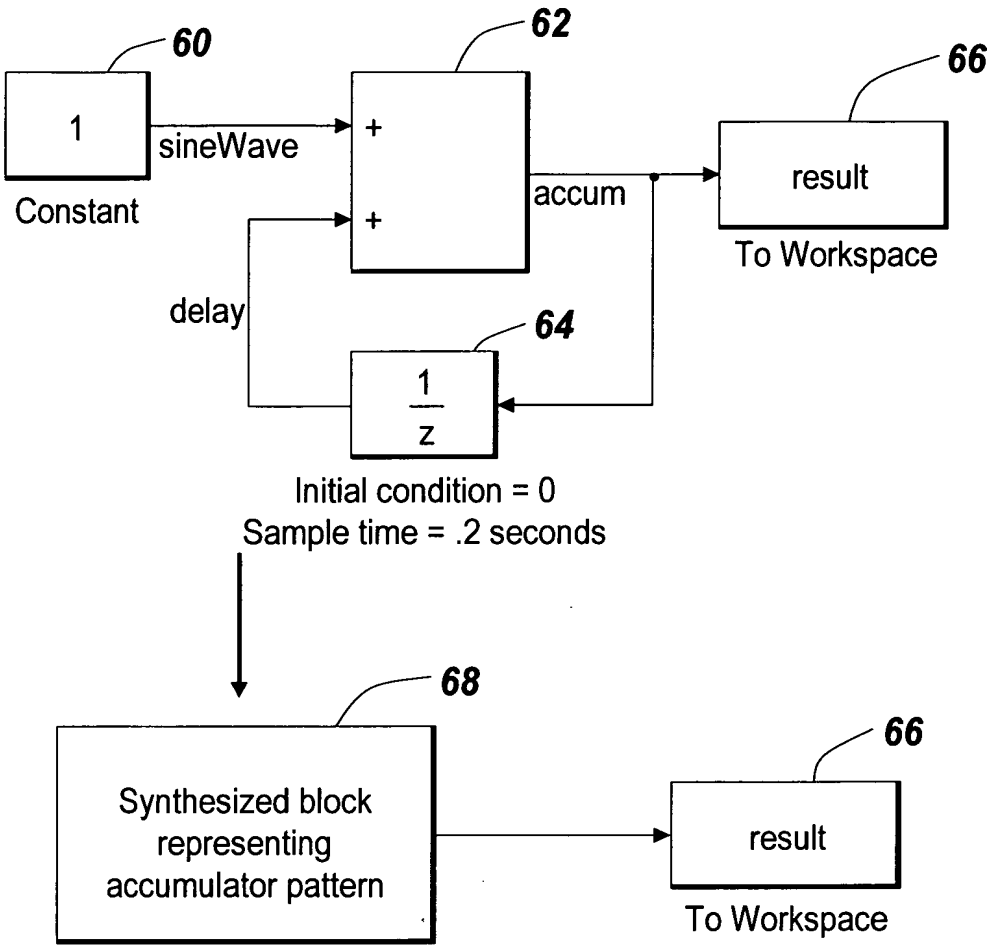

Fig. 5
(prior art)

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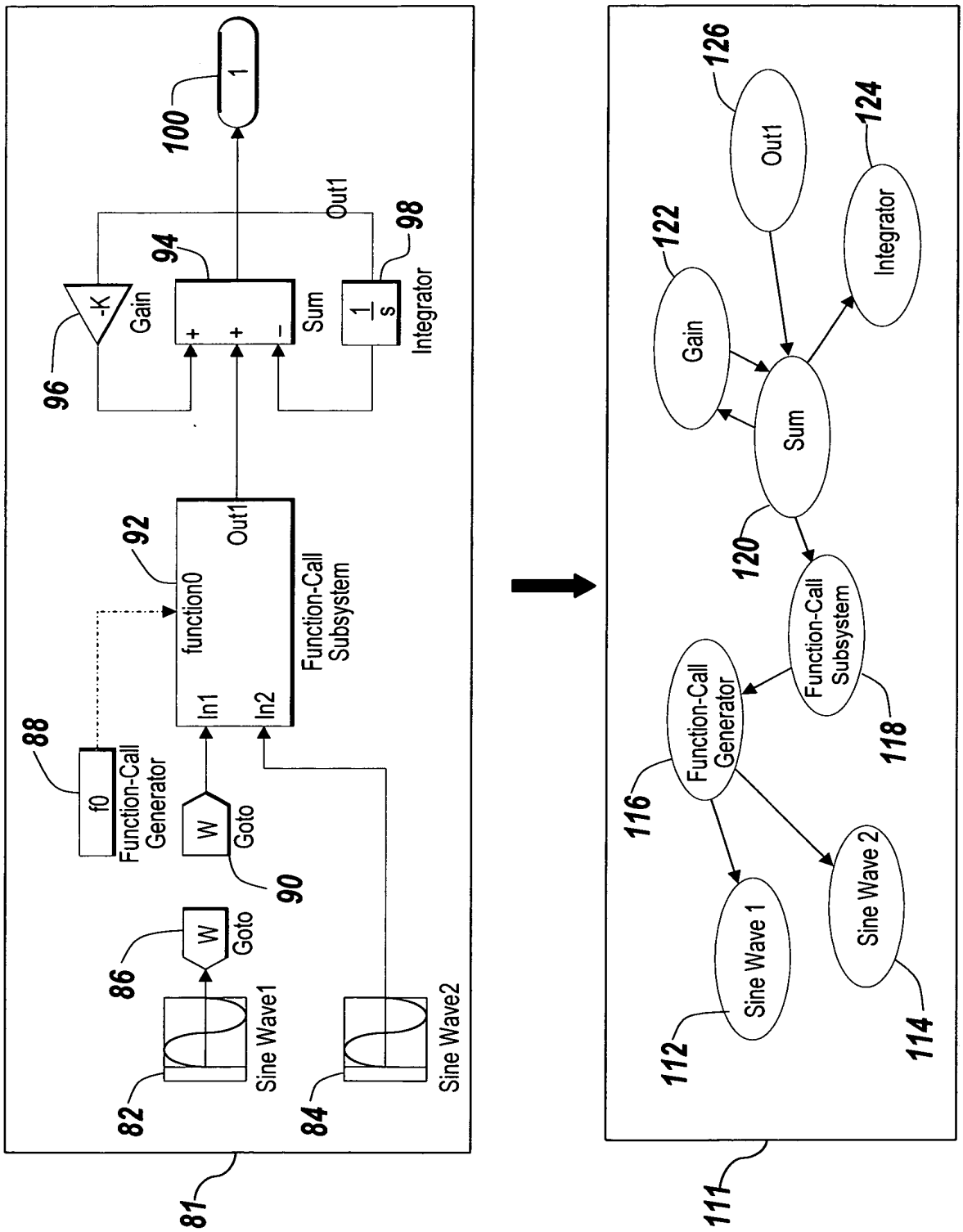

Fig. 6A
(prior art)

Sorted List:

0:0 Sine Wave 1
0:1 Sine Wave 2
0:2 Function-Call Generator
0:3 Function-Call Subsystem
0:4 Integrator
0:5 Gain (algebraic id 0#1)
0:6 Sum (algebraic variable for id 0#1)
0:7 Out1

Fig. 6B
(prior art)

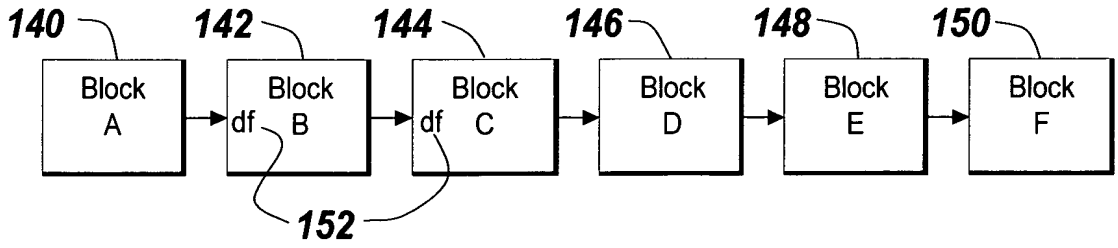

Fig. 7A
(prior art)

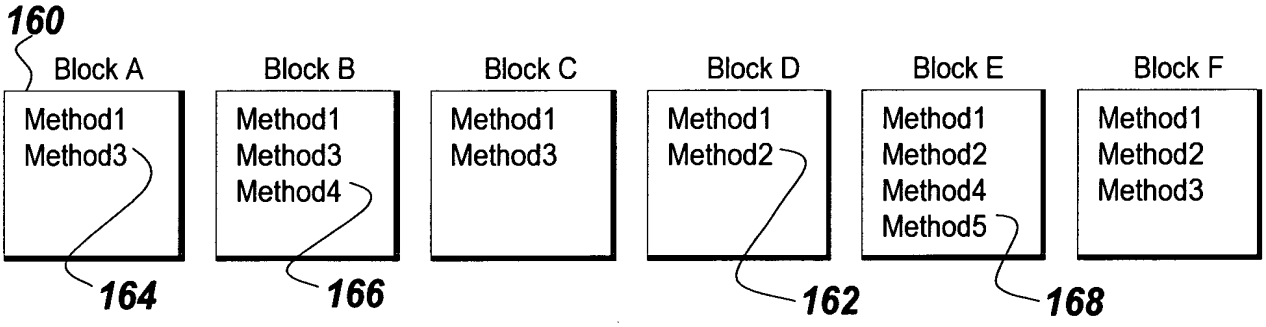

Fig. 7B
(prior art)

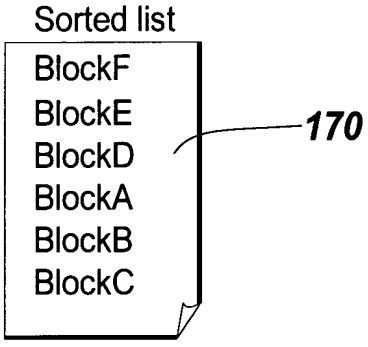

Fig. 7C
(prior art)

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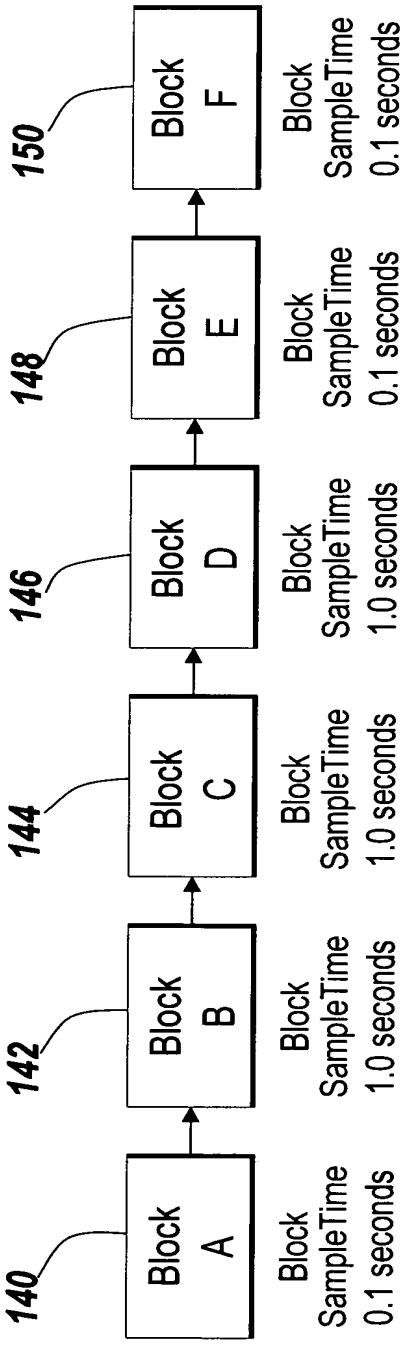

Fig. 8
(prior art)

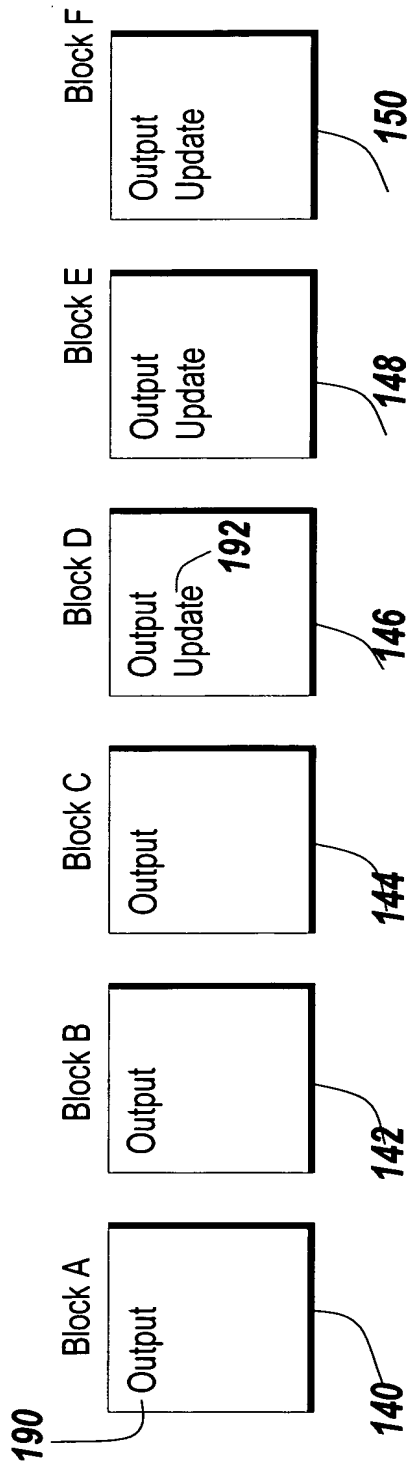

Fig. 9
(prior art)

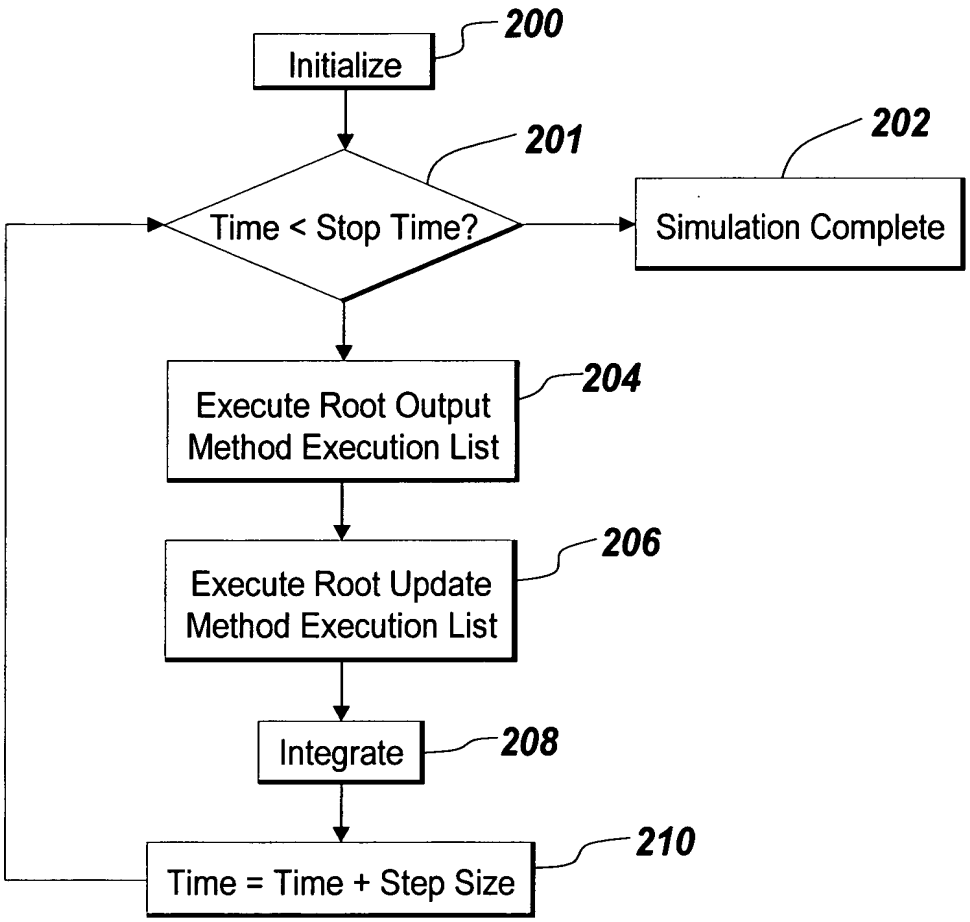

Fig. 10
(prior art)

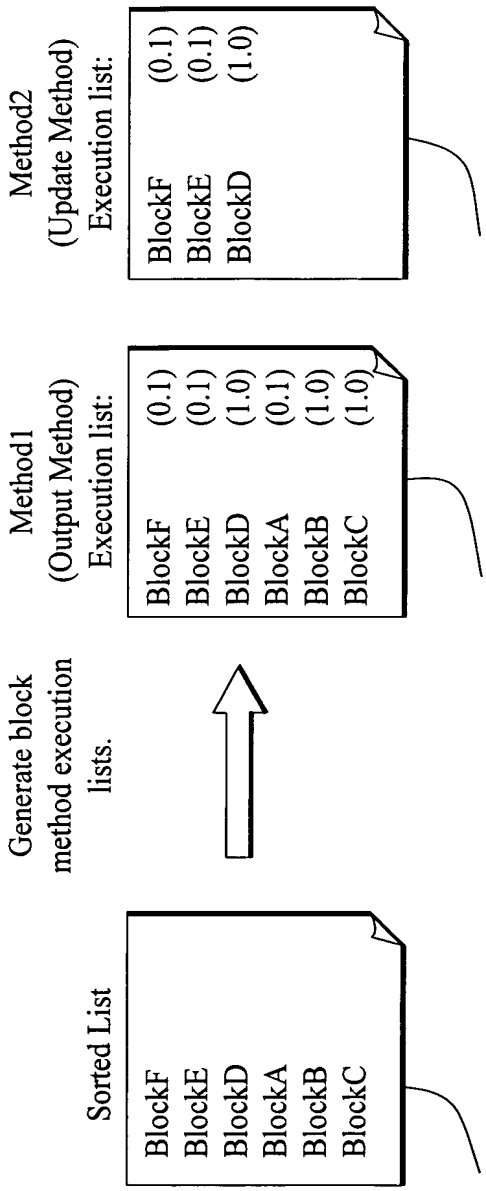

Fig. 11A
(prior art)

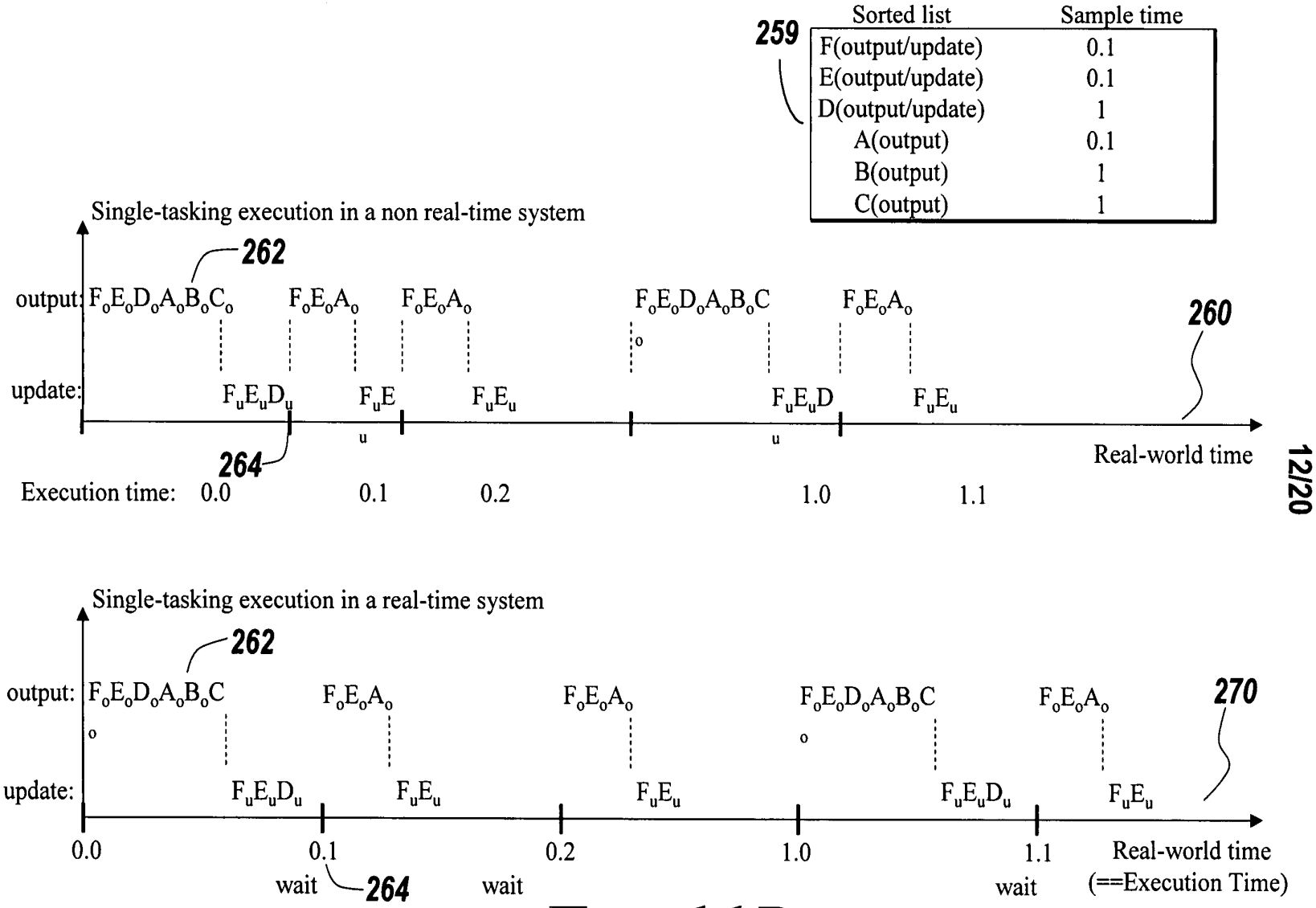

Fig. 11B

(prior art)

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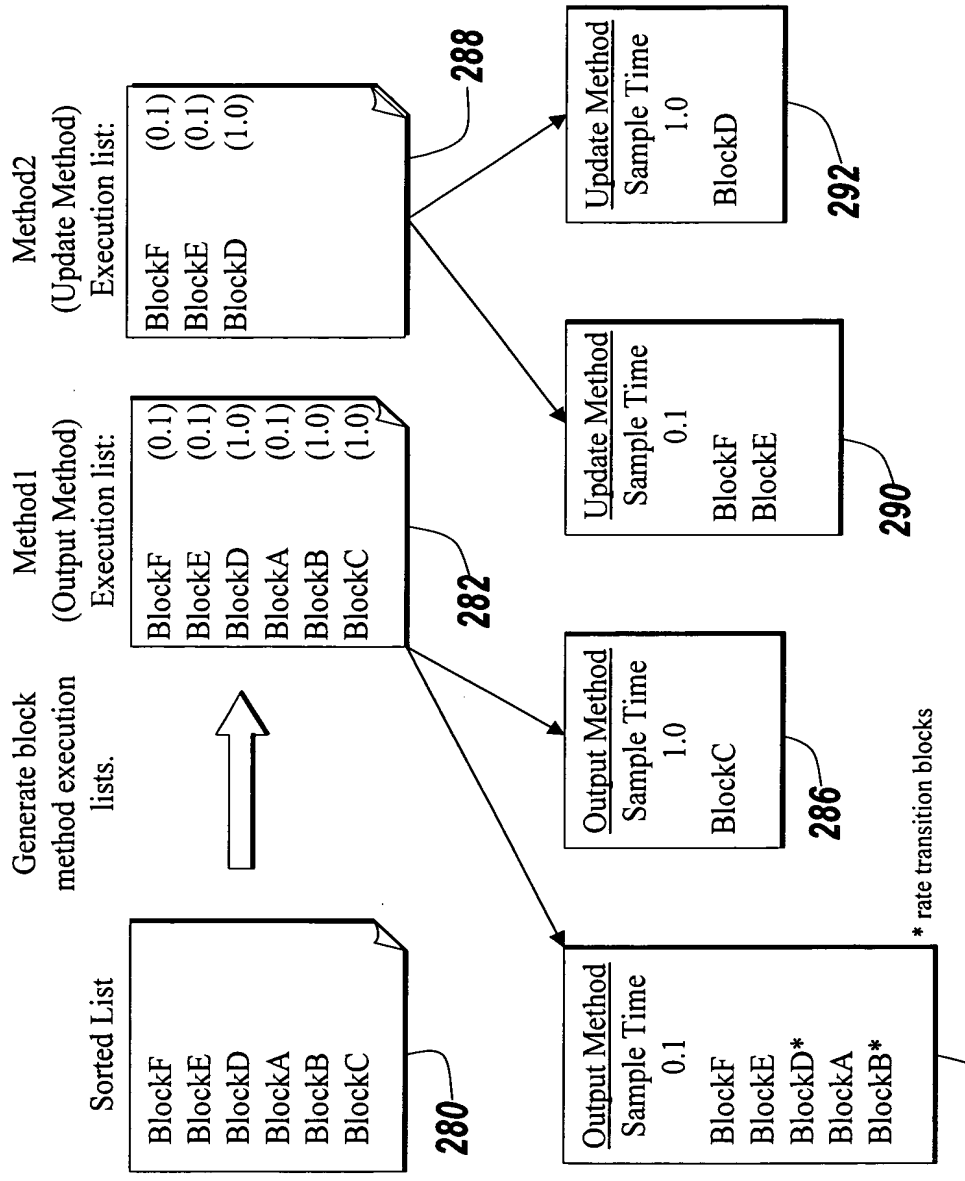

Fig. 12A
(prior art)

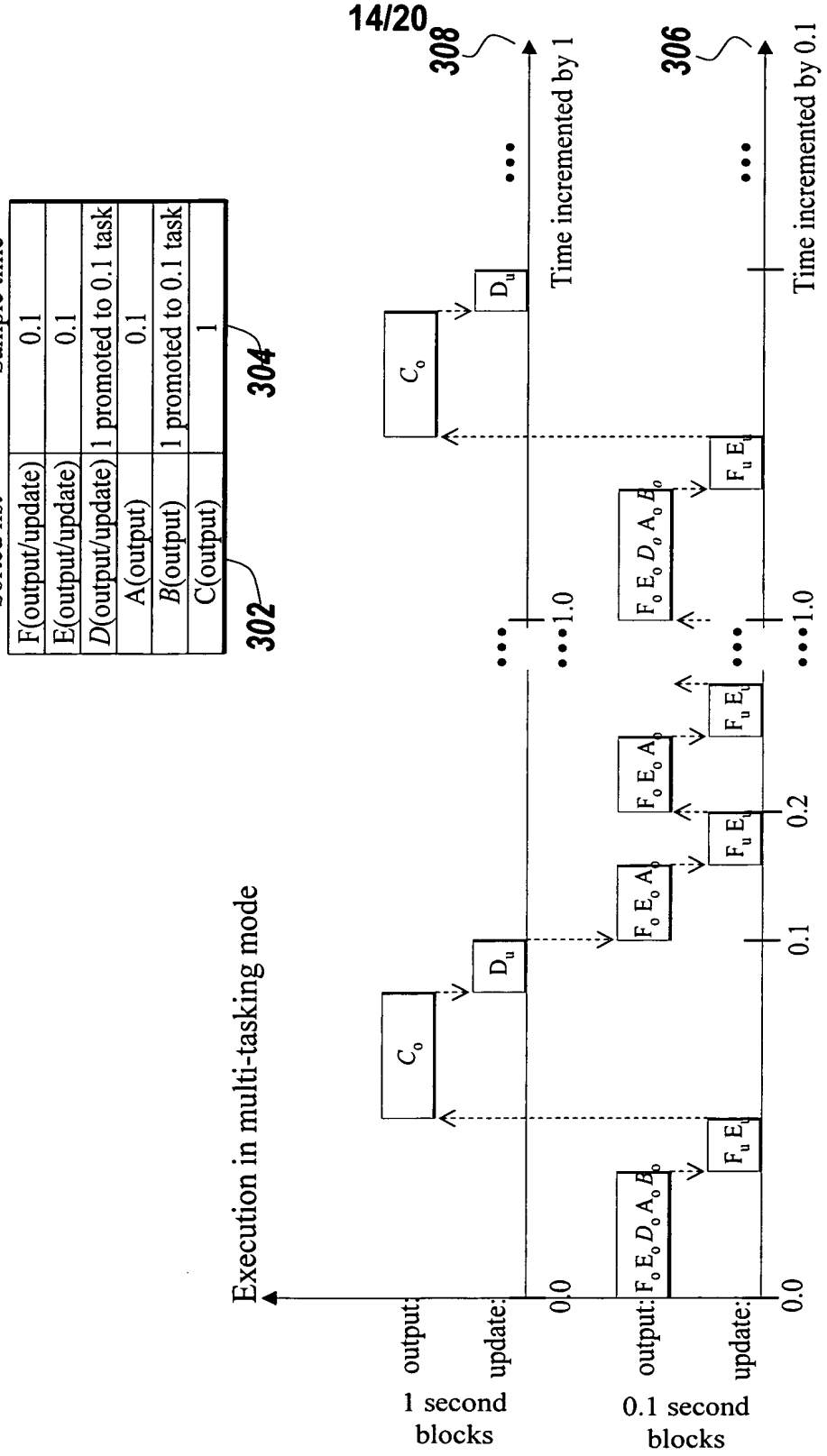

Fig. 12B
(prior art)

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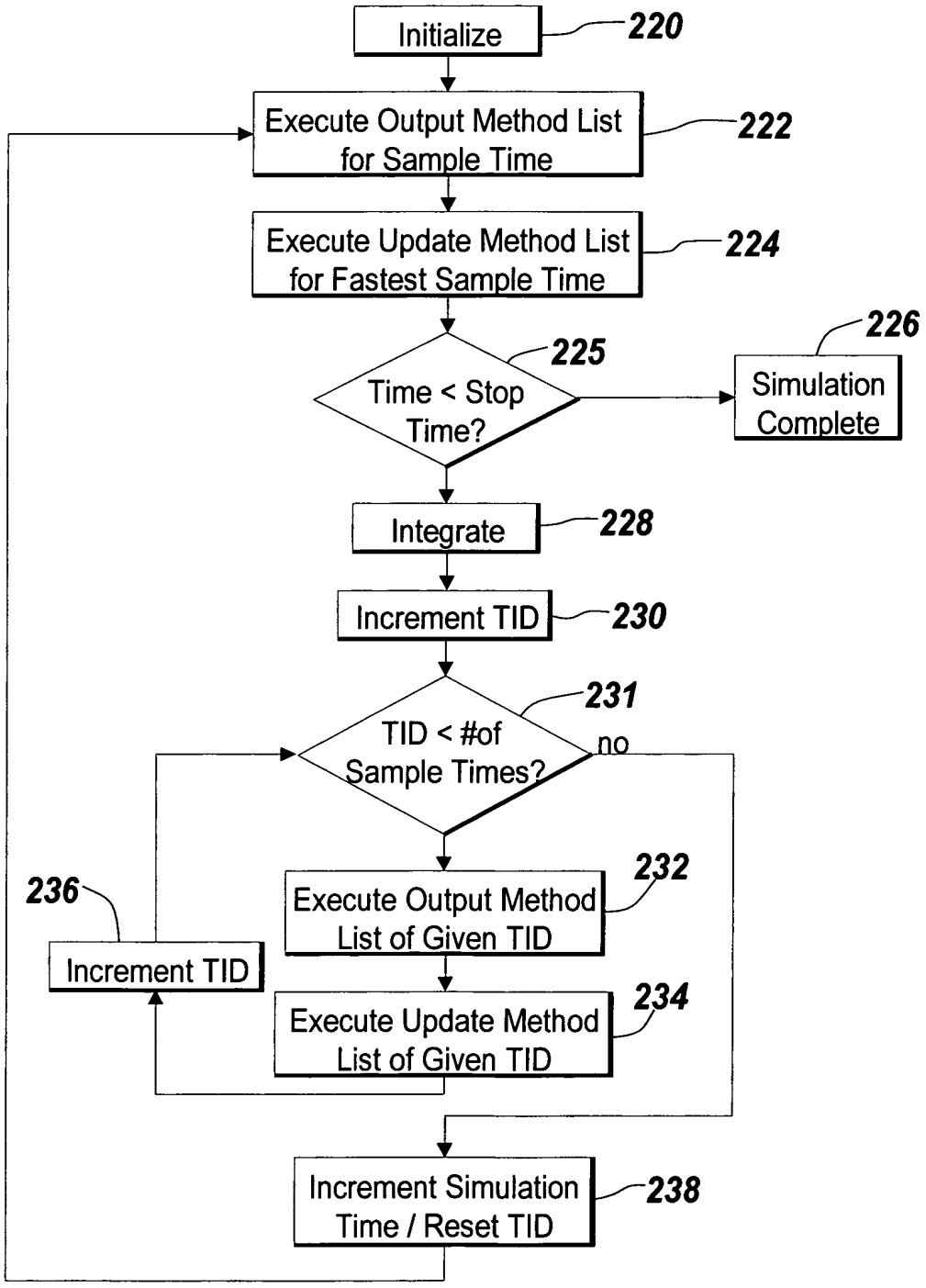

Fig. 13
(prior art)

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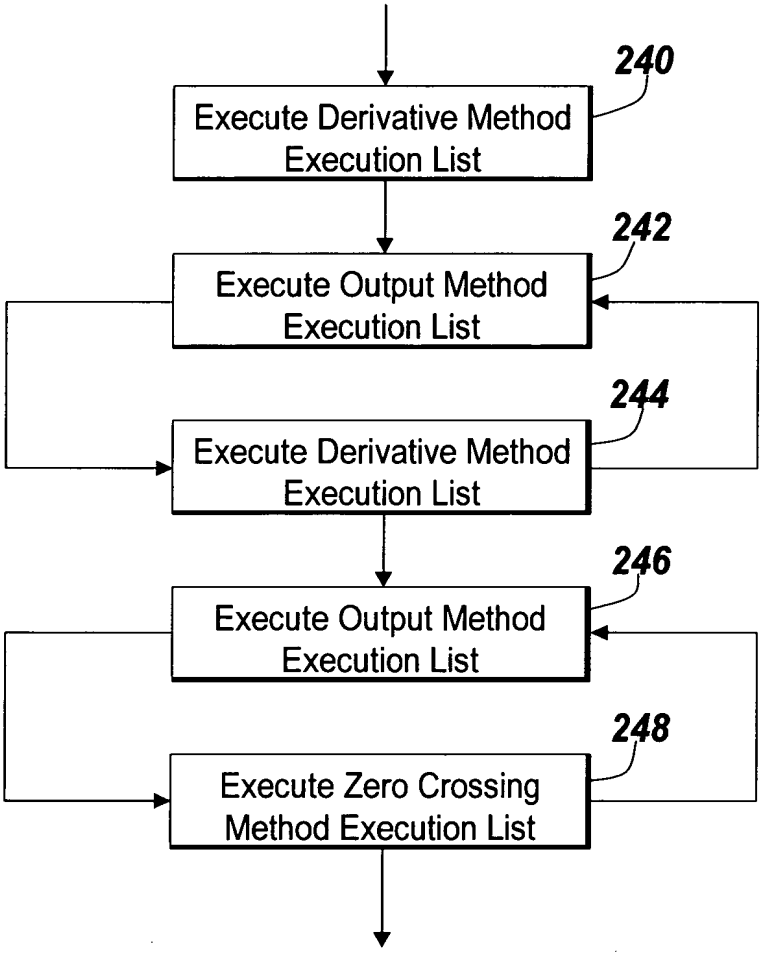

Fig. 14
(prior art)

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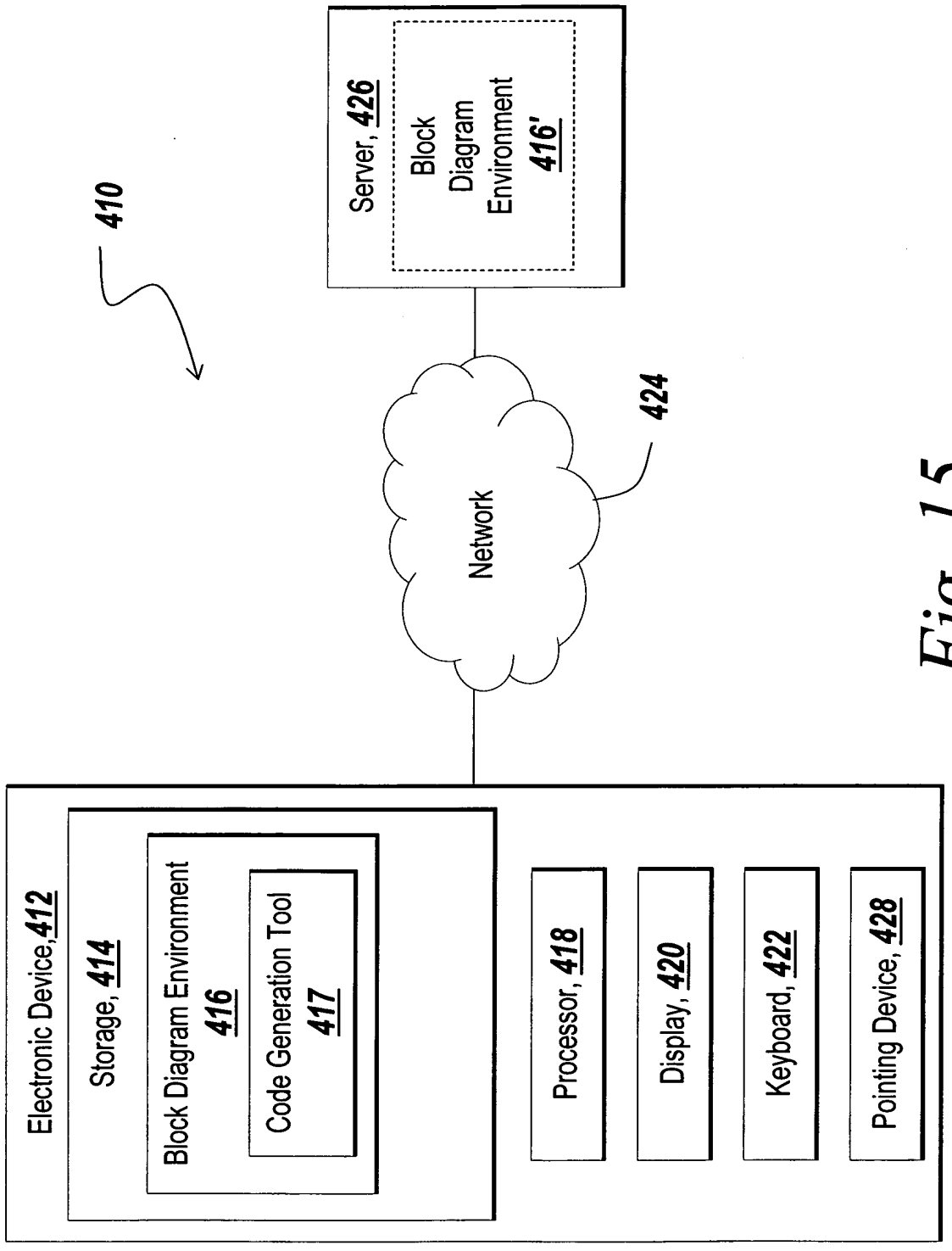
410
Server, 426
Block Diagram Environment 416'
424
Network
Electronic Device, 412
Storage, 414
Block Diagram Environment 416
Code Generation Tool 417
Processor, 418
Display, 420
Keyboard, 422
Pointing Device, 428

Fig. 15